Matter No.: 10449-028002 Applicant: Koji Hayashi CONTROLLER FOR DATA RECORDER

Personal Computer Buffer Underrun Determi -nation Recording Contro Access Control interface 22 Restart , Signal Restart Signal Location Detection Detection Buffer Memory Retry Determi -nation Location 45 46 Address Memory Address Interrupt Signal Memory 8 47 Interrupt /Restart Subcode Synchro -nizing Signal Subcode (M) Encoder 43 Signal Synchro -nization RAM 15 Operational Clock Generation System Clock Laser Orive Crystal Oscillation Subcode Synchro -nizing Signal Pit Clock Subcode Orive Signal 18 Spindle Motor Wobble Decorder Decoding Decorder Decoding Optica Subcode Head Signal ATIP 딾 Amp Spindle Servo Rotation Control Signal œ Head Servo 胎 Digtal Data

## Fig.2(a)

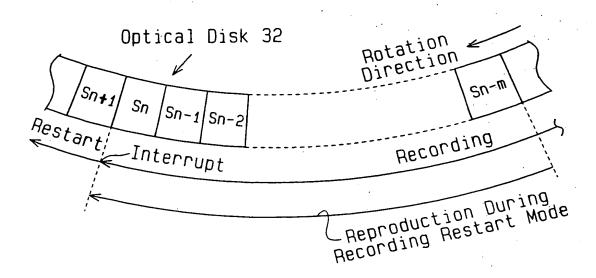


Fig.2(b)

